

What is claimed is:

1. A compiling method, comprising
compiling source code into source binary code for a first computing
platform; and
5 generating an annotation section associated with the source binary code,
the annotation section comprising an annotation for a scope, the scope
comprising at least one block of the source binary code having at least one
attribute to aid a translator optimization.
- 10 2. The compiling method of claim 1, wherein if the scope comprises a
plurality of blocks, the blocks have consecutive addresses with each other and
have the at least one attribute in common.
- 15 3. The compiling method of claim 1, wherein the annotation section further
comprises a region annotation for a region comprising one or more scope.
- 20 4. The compiling method of claim 3, wherein the annotation for the scope
further comprises scope addresses, scope size and the at least one attribute, and
the annotation for the region further comprises a region annotation pointer, region
addresses and region size.
5. The compiling method of claim 1, wherein the attribute comprises
information associated with register spilling and restoring instructions within a
block.

6. The compiling method of claim 1, wherein the attribute comprises information associated with local variable assignment within a block.

5 7. The compiling method of claim 1, wherein the attribute comprises information associated with volatile variable access within a block.

8. A translating method, comprising:

 inputting source binary code for a first computing platform and an
10 annotation section associated with the source binary code; and
 translating the source binary code to a target binary code for a second
computing platform by utilizing the annotation section,
 wherein, an annotation section comprises an annotation for a scope, the
scope comprising at least one block of the source binary code having at least one
15 attribute to aid a translator optimization.

9. The translating method of claim 8, wherein if the scope comprises a plurality of blocks, the blocks have consecutive addresses with each other and have the at least one attribute in common.

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10. The translating method of claim 8, wherein the annotation section further comprises an annotation for a region comprising one or more scope.

11. The translating method of claim 10, wherein the annotation for the
25 scope further comprises scope addresses, scope size and the at least one

attribute, and the annotation for the region further comprises a region annotation pointer, region addresses and region size.

12. The translating method of claim 8, wherein the attribute comprises
5 information associated with register spilling and restoring instructions within a block.

13. The translating method of claim 8, wherein the attribute comprises
information associated with local variable assignment within a block.
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14. The translating method of claim 8, wherein the attribute comprises
information associated with volatile variable access within a block.

15. The translating method of claim 8, wherein translating the source binary
15 code for the source platform further comprises:
generating target intermediate code based upon the source binary code;
optimizing the target intermediate code by utilizing the annotation section;
and
generating the target binary code for the target platform based upon the
20 optimized target intermediate code.

16. The translating method of claim 15, wherein optimizing the target
intermediate code further comprises:
generating an internal representation for the annotation section in response
25 to determining that the internal representation has not been established;

reading from the annotation section an attribute associated with a block of the target intermediate code based upon the internal representation; and optimizing the block based upon the read attribute.

5 17. The translating method of claim 16, wherein the internal representation is an AVL tree, a node of the AVL tree comprising region addresses and region annotation pointer.

18. A compiler, comprising:

10 a compiling component to compile source code into source binary code for a first computing platform;

 an annotation generator to generate an annotation section associated with the source binary code, the annotation section comprising an annotation for a scope, the scope comprising at least one block of the source binary code having
15 at least one attribute to aid a translator optimization.

19. The compiler of claim 18, wherein if the scope comprises a plurality of blocks, the blocks have consecutive addresses with each other and have the at least one attribute in common.

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20. The compiler of claim 18, wherein the annotation section further comprises an annotation for a region comprising one or more scope.

21. The compiler of claim 20, wherein the annotation for the scope further
25 comprises scope addresses, scope size and the at least one attribute, and the

annotation for the region further comprises a region annotation pointer, region addresses and region size.

22. The compiler of claim 18, wherein the attribute comprises information
5 associated with register spilling and restoring instructions within a block.

23. The compiler of claim 18, wherein the attribute comprises information associated with local variable assignment within a block.

10 24. The compiler of claim 18, wherein the attribute comprises information associated with volatile variable access within a block.

25. A translator, comprising:

an input component to input source binary code for a first computing
15 platform and an annotation section associated with the source binary code; and
a translate component to translate the source binary code to target binary code for a second computing platform by utilizing the annotation section,

wherein, the annotation section comprises an annotation for a scope, the scope comprising at least one block of the source binary code having at least one
20 attribute to aid a translator optimization.

26. The translator of claim 25, wherein if the scope comprises a plurality of blocks, the blocks have consecutive addresses with each other and have the at least one attribute in common.

27. The translator of claim 26, wherein the annotation section further comprises an annotation for a region comprising one or more scope.

28. The translator of claim 27, wherein the annotation for the scope further
5 comprises scope addresses, scope size and the at least one attribute, and the annotation for the region further comprises a region annotation pointer, region addresses and region size.

29. The translator of claim 25, wherein the translate component generates
10 target intermediate code based upon the source binary code; optimizes the target intermediate code by utilizing the annotation section; and generates the target binary code for the target platform based upon the optimized target intermediate code.

15 30. The translator of claim 25, wherein the translate component generates an internal representation for the annotation section in response to determining that the internal representation has not been established; read from the annotation section an attribute associated with a block of the target intermediate code based upon the internal representation; and optimizes the block based upon the read
20 attribute.